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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Kannan Raj et al.

Serial No.: 09/839,023

Filed: April 20, 2001

For: Optically Interconnecting
Multiple Processors

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Art Unit: 2633

Examiner: Dalzid E. Singh

Docket: ITL.0462US
P9816

Assignee: Intel Corporation

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed August 24, 2005, reconsideration is requested in view of the following remarks.

Date of Deposit: September 7, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden
Cynthia L. Hayden